

7. A system comprising:

a plurality of agents;

an external non-dedicated shared memory block accessible by each of said plurality of agents, said external non-dedicated shared memory block including a plurality of memory banks;

a register adapted to partition said external non-dedicated shared memory block into a plurality of partitions each of said plurality of partitions being accessible by a unique group of said plurality of agents; and

said plurality of partitions each comprise a number of said plurality of memory banks;

wherein said plurality of agents receive a base clock signal from another agent and have variable clock signal representations of a base clock signal.

13. A system for providing access to shared external non-dedicated memory, said system comprising:

a first agent to provide a memory access clock signal to allow said first agent to access said shared external non-dedicated memory; and

a second agent, receiving said memory access clock signal from said first agent, to provide a representation of said memory access clock signal to access said shared external non-dedicated memory in synchronism with said access by said first agent to said shared external non-dedicated memory;

wherein each of said first agent and said second agent may access different portions of said shared external non-dedicated memory simultaneously.

17. A method of synchronizing access from a plurality of agents to external non-dedicated shared memory, comprising:

providing a memory access clock signal;

providing a representation of said memory access clock signal in synchronism with said memory access clock signal;

firstly accessing a portion of said external non-dedicated shared memory from a first agent based on said memory access clock signal;

secondly accessing a portion of said external non-dedicated shared memory from a second agent based on said representation of said memory access clock signal received from said first agent;

wherein said step of secondly accessing said external non-dedicated shared memory follows said step of firstly accessing without a wait state therebetween.

Version with Markings to Show Changes Made

1. (Three Times Amended) A system comprising:
an external non-dedicated memory including a plurality of memory banks;

a first agent having a clock signal adapted to access a first memory portion including a first number of said plurality of memory banks; and

a second agent, receiving said memory access clock signal from said first agent, having a clock signal representation of said first agent's clock signal adapted to access a second memory portion including a second number of said plurality of memory banks;

said first number and said second number being variable.

7. (Three Times Amended) A system comprising:
a plurality of agents;
an external non-dedicated shared memory block accessible by each of said plurality of agents, said external non-dedicated shared memory block including a plurality of memory banks;

a register adapted to partition said external non-dedicated shared memory block into a plurality of partitions each of said plurality of partitions being accessible by a unique group of said plurality of agents; and

said plurality of partitions each comprise a number of said plurality of memory banks;

wherein said plurality of agents receive a base clock signal from another agent and have variable clock signal representations of a base clock signal.

13. (Three Times Amended) A system for providing access to shared external non-dedicated memory, said system comprising:

a first agent to provide a memory access clock signal to allow said first agent to access said shared external non-dedicated memory; and

a second agent, receiving said memory access clock signal from said first agent, to provide a representation of said memory access clock signal to access said shared external non-dedicated memory in synchronism with said access by said first agent to said shared external non-dedicated memory;

wherein each of said first agent and said second agent may access different portions of said shared external non-dedicated memory simultaneously.

17. (Three Times Amended) A method of synchronizing access from a plurality of agents to external non-dedicated shared memory, comprising:

providing a memory access clock signal;

providing a representation of said memory access clock signal in synchronism with said memory access clock signal;

firstly accessing a portion of said external non-dedicated shared memory from a first agent based on said memory access clock signal;

secondly accessing a portion of said external non-dedicated shared memory from a second agent based on said representation of said memory access clock signal received from said first agent;

wherein said step of secondly accessing said external non-dedicated shared memory follows said step of firstly accessing without a wait state therebetween.